Dr Gross is an independent contractor providing technical and programmatic support to the Defense Advanced Research Projects Agency in the Microsystems Technology Office for the last ten years. He has been involved in numerous programs covering vacuum and high speed electronics, phased arrays, mm-wave and terahertz devices and circuits, GaN process and foundry maturation, RF and microwave filters and signal cancellers, heterogenous fabrication, and programmable logic devices. Prior to DARPA, Dr. Gross worked at the MEMS and Nanotechnology Exchange and Intel Corporation. He is a senior member of the IEEE and holds a Ph.D. in Electrical Engineering with a concentration in Micro-Electro-Mechanical Systems (MEMS) and solid-state devices, and a bachelor of science in Mechanical Engineering.